

Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 109503		APPLICATION NO. 09/868,322	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANT Yojiro MATSUEDA			
				FILING DATE June 18, 2001		GROUP 2674	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
JN	1	6,873,312 B2	03-29-2005	Matsueda			
JN	2	6,025,822	02-15-2000	Motegi et al.			
JN	3	5,815,136	09-29-1998	Ikeda et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
JN	4	EP 0456394 A2	11-13-1991	Europe			
JN	5	EP 0488851 A1 w/ English Abstract	06-03-1992	Europe			
JN	6	EP 0286309 A2	10-12-1988	Europe			
JN	7	JP-A-11-058065 w/ English Abstract	03-02-1999	Japan			
JN	8	JP-A-08-227283 w/ English Abstract	09-03-1996	Japan			
JN	9	JP-A-281636 w/ English Abstract	10-27-1995	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
JN	10	R. Abbott et al., "Equipping a Line of Memories With Spare Cells," Electronics, VNU Business Publications, vol. 54, no. 15, pgs. 127-130, July 1981					
EXAMINER				DATE CONSIDERED			
<i>Jenifer J. [Signature]</i>				9/27/05			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: July 13, 2005